

S/N 10/642,702

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Vivek Jaiswal et al.	Examiner:	Chirag R. Patel
Serial No.:	10/642,702	Group Art Unit:	2141
Filed:	August 18, 2003	Docket:	P16507
Title:	DYNAMIC LOAD DISTRIBUTION WITHIN A SESSION INITIATION PROTOCOL NETWORK		
Assignee:	Intel Corporation		

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants have received no response to date to Applicants' Notice of Appeal and Pre-Appeal Brief Conference Request submitted six months ago.

Kindly inform us as to the status of this application, directing such notice to the attention of the below-signed attorney for Applicants.

Respectfully submitted,

VIVEK JAISWAL ET AL.

By their Representatives,

Customer Number: 76973

Tel. 817-281-7131

Date: May 16, 2008

By: /Christopher K. Gagne, Reg. No. 36,142/
Christopher K. Gagne
Reg. No. 36,142